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L1 same (message or instruction or command)	32

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<u>L2</u>	L1 same (message or instruction or command)	32	<u>L2</u>
<u>L1</u>	processor same initializ\$3 same controller same disabl\$3	69	<u>L1</u>

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<u>L3</u>	L2	0	<u>L3</u>
DB=PGPB,USPT,USOC; PLUR=YES; OP=OR			
<u>L2</u>	L1 same (message or instruction or command)	32	<u>L2</u>
<u>L1</u>	processor same initializ\$3 same controller same disabl\$3	69	<u>L1</u>

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Search Results -

Terms	Documents
(361/683 361/686 370/257 370/453 709/208 710/305 710/104 710/313 710/314 710/315 710/10 710/110 711/100 711/147 711/154 712/32 712/36 719/321 719/327 713/1).ccls.	14840

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315,10,110;711/100,147,154;361/683,686;713/1;709/208;719/321,327;712/32,36;370/257,453.ccls. 14840 L1

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L1 and L2	7

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<i>DB=PGPB,USPT,USOC; PLUR=YES; OP=OR</i>		
<u>L3</u> 11 and L2	7	<u>L3</u>
<u>L2</u> processor same initializ\$3 same controller same disabl\$3	69	<u>L2</u>
<u>L1</u> 710/305,104,313- 315,10,110;711/100,147,154;361/683,686;713/1;709/208;719/321,327;712/32,36;370/257,453.ccls.	14840	<u>L1</u>

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Schuchman, E.; Vijaykumar, T.N.;
Computer Architecture, 2005. ISCA '05. Proceedings. 32nd International Symposium on
04-08 June 2005 Page(s):160 - 171
Digital Object Identifier 10.1109/ISCA.2005.44

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Chien-Chi Chen; Jen-Chien Chien; Meng-Lun Hsueh; Jer-Junn Luh; Fok-Ching Chong;
Engineering in Medicine and Biology Society, 2003. Proceedings of the 25th Annual International Conference of the IEEE
Volume 2, 17-21 Sept. 2003 Page(s):1732 - 1733 Vol.2
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Regehr, J.; Reid, A.; Webb, K.; Parker, M.; Lepreau, J.;
Real-Time Systems Symposium, 2003. RTSS 2003. 24th IEEE
2003 Page(s):25 - 36
Digital Object Identifier 10.1109/REAL.2003.1253251

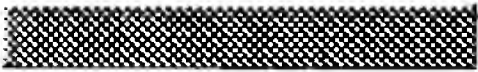
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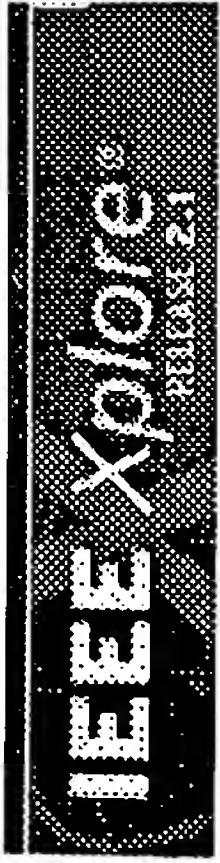


10. **A low power pseudo-random BIST technique**

Basturkmen, N.Z.; Reddy, S.M.; Pomeranz, I.;
Computer Design: VLSI in Computers and Processors, 2002. Proceedings. 2002 IEEE International Conference on
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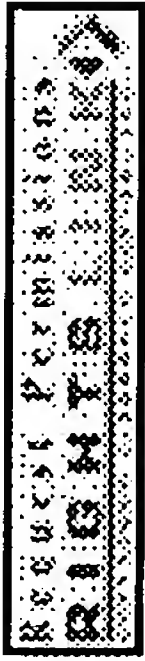
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Inst. of Electr. Eng., Nat. Taiwan Univ., China

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Publication Date: 17-21 Sept. 2003
Volume: 2
On page(s): 1732 - 1733 Vol.2
Number of Pages: 4295
ISSN: 1094-687X
INSPEC Accession Number: 7994052
Digital Object Identifier: 10.1109/EMBS.2003.1279734
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Abstract

The multiple masters multiple slave (M3S) is an intelligent protocol. It provides the disable a concrete integral control for his movement, working, environment control and communication. This is an integrated real time control capability protocol. It is a plug and play device interface. Once plug-in, the new device with integrate and communicate with existing devices in use. Our proposal is aim at this purpose. We intend to design and develop a M3S protocol that meets the requirement lay down in the M3S working group. Integrating different types of tools will absolutely bring lots of comforts to the disable. We set up the system using equipments purchase from the market, tested the system and ready it to be tested by other subgroups.

Index Terms
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Non-controlled Indexing

M3S novel kernel system assistive device communication concrete integral control digital signal processor environment control integrated real time control capability protocol multiple masters multiple slave protocol

Author Keywords

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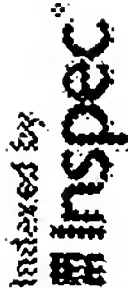
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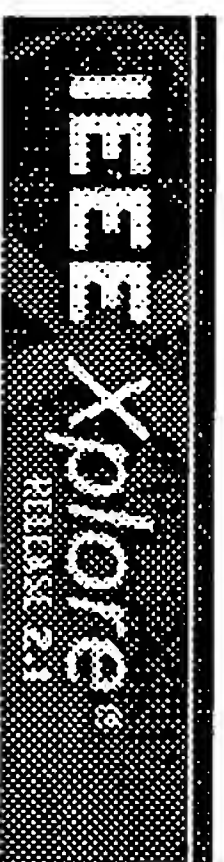
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